FIG. 1 (PRIOR ART)

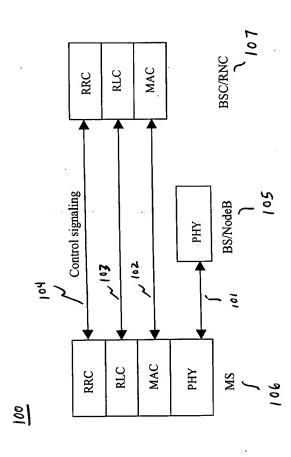
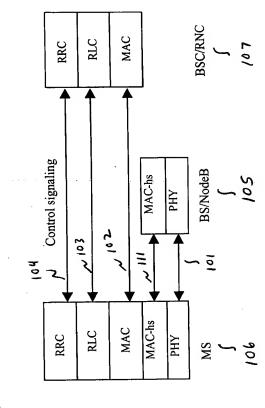
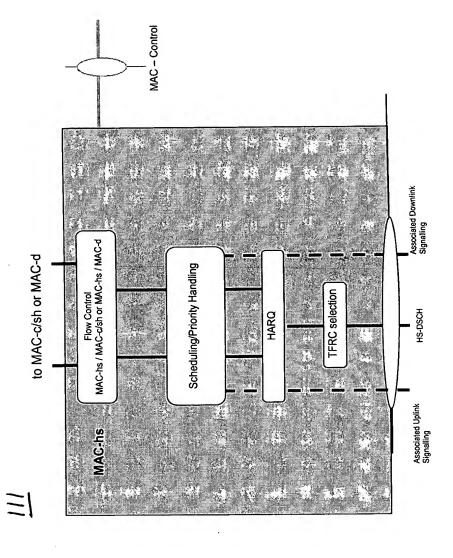


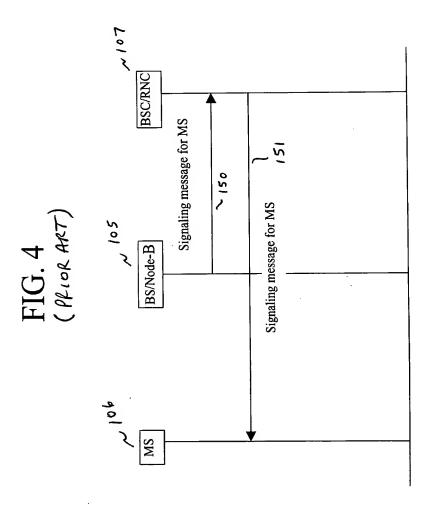
FIG. 2 (PRIOR ART)



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FIG. 3 (PRIOR ART)





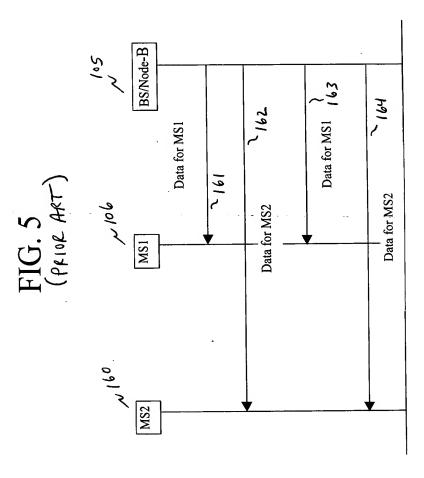


FIG. 6

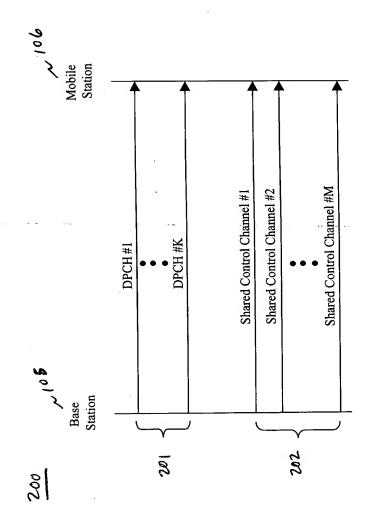
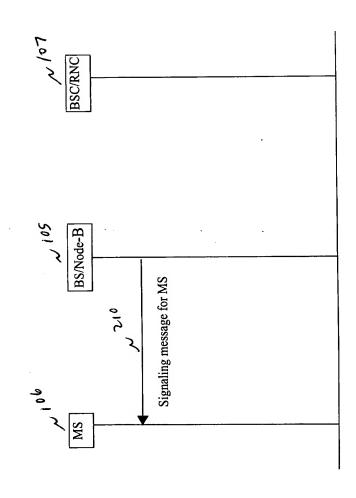
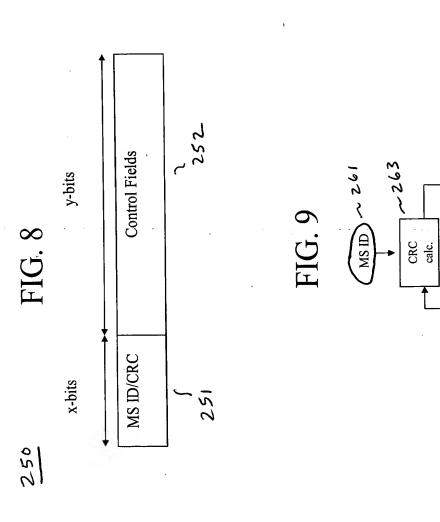


FIG. 7





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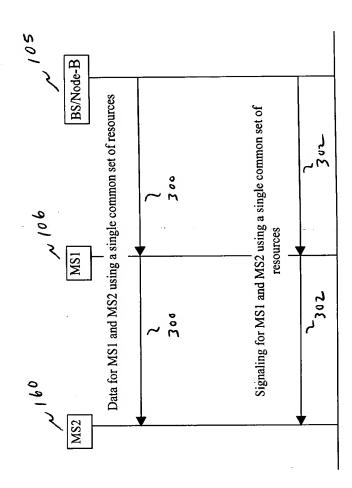
CRC

SCCH control fields

x bits

y bits

FIG. 10



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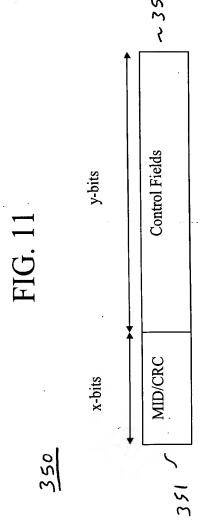


FIG. 12

